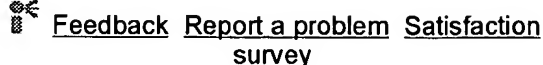




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Chandrasekhar Boyapati, Alexandru Salcianu, William Beebee, Martin Rinard

May 2003 **ACM SIGPLAN Notices**, Proceedings of the ACM SIGPLAN 2003 conference on Programming language design and implementation, Volume 38 Issue 5

Full text available: pdf(375.18 KB)

Additional Information: full citation, abstract, references, citings, index terms

The Real Time Specification for Java (RTSJ) allows a program to create real-time threads with hard real-time constraints. Real-time threads use region-based memory management to avoid unbounded pauses caused by interference from the garbage collector. The RTSJ uses runtime checks to ensure that deleting a region does not create dangling references and that real-time threads do not access references to objects allocated in the garbage-collected heap. This paper presents a static type system that ...

Keywords: encapsulation, ownership types, real-time, regions

2 Error-free garbage collection traces: how to cheat and not get caught

Matthew Hertz, Stephen M Blackburn, J Eliot B Moss, Kathryn S. McKinley, Darko Stefanović

June 2002 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 2002 ACM SIGMETRICS international conference on Measurement and modeling of computer systems.** Volume 30 Issue 1

Full text available: pdf (105.06 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#)

Programmers are writing a large and rapidly growing number of programs in object-oriented languages such as Java that require garbage collection (GC). To explore the design and evaluation of GC algorithms quickly, researchers are using simulation based on traces of object allocation and lifetime behavior. The

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Optimizing dynamic memory management in a multithreaded application executing on a multiprocessor

Haggander, D. Lundberg, L.

Ericsson Software Technol. AB, Karlskrona;

This paper appears in: Parallel Processing, 1998. Proceedings. 1998 International Conference on

Meeting Date: 08/10/1998 -08/14/1998

Publication Date: 10-14 Aug 1998

Location: Minneapolis, MN , USA

On page(s): 262-269

References Cited: 13

IEEE Catalog Number: 98EX205

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INSPEC Accession Number: 6034718

Abstract:


The Billing Gateway (BGw) is a large multithreaded object oriented C++ appli running on Sun Solaris. Due to frequent allocation and deallocation of dynami memory, the initial implementation of this system suffered from poor perform when executed on a multiprocessor. We compare two approaches for improv performance of BGw. First we replace the standard Solaris heap with a paralle heap. In the second approach we optimize the application code by removing a number of heap allocations/deallocations. In order to do this, we introduce m pools for commonly used object types and replace some heap variables with s variables. The parallel heap approach resulted in a dramatic speedup improve The optimization of the application code did also result in a dramatic speedup improvement. For this approach the performance using a single processor computer was also increased by a factor of eight. The optimizations took approximately one week to implement

Index Terms:

data structures multiprocessing systems object-oriented programming storage allocat storage management Billing Gateway Sun Solaris commonly used object types dyna memory allocation dynamic memory management optimization heap allocations/deallocations heap variables large multithreaded object oriented C++ applic memory pools multiprocessor multithreaded application parallel heap parallel heap approach single processor computer stack variables standard Solaris heap

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Parallel Processing, 1998. Proceedings. 1998 International Conference on , 10-1 1998

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2 A hardware implementation of realloc function

Witawas Srisa-An; Chia-Tien Dan Lo; Chang, J.M.;

VLSI '99. Proceedings IEEE Computer Society Workshop On , 8-9 April 1999

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3 Dynamic memory management for real-time embedded Java chips

Chi-Min Lin; Tien-Fu Chen;

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4 Scalable hardware-algorithm for mark-sweep garbage collection

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Euromicro Conference, 2000. Proceedings of the 26th , Volume: 1 , 5-7 Sept. 2

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6 A high-performance memory allocator for memory intensive applicati

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